Lexmark RBA BBM User Manual

General Description and Name

This BBM is using the RBA method of bad block management. Besides to calculate the spare area part with ECC and CRC, a BB Table is also needed to be created to store the Bad Block information.

Relevant User Options

The following special features on the special features tab apply to this scheme. The default values might work in some cases but please make sure to set the right value according to your system.
Please note only the below special feature items are related to this scheme and ignore any others. If any of below items doesn't exist, please check whether the right version has been installed or contact Data I/O for support by submitting Device Support Request through this address:
http://www.dataio.com/support/dsr.asp

<u>Bad Block Handling Type</u> =   "Lexmark RBA"

<u>Spare area</u> :   Please refer to "Description of common NAND special features.pdf". ***Normally set as " ECC " for this BBM***.[Default 'Disabled']

<u>Check BB Mark In DataFile</u> :   Please refer to "Description of common NAND special features.pdf". ***Normally set as " Disabled" for this BBM***.[Default 'Enabled']

<u>bad block detection</u> :   Please refer to "Description of common NAND special features.pdf". ***Normally set as " BBM then BB marker" for this BBM while do re-program***.

<u>Lex: Data Swap Mode</u> :   Indicate how to swap the PC file into the device. ***Normally set as " DWORD Swap Mode" for this BBM.***

<u>Lex: Next Block of Reserved Area</u> : The NEXT block index after the reserved area. This item is used to specify the reserved area location and normally keep its value as the block amount of the device. [Default as device block amount]

<u>Lex: Number of Reserved blocks</u> :   the Number of blocks reserved for the bad blocks. Normally as the maximum bad blocks allowed for the device.

<u>Max Error Bits Per Block</u> :   the maximum allowed error-bits per block. Normally set as the 20 for this BBM. [Default 20 if listed, otherwise no such limitation.]

No Error Bits Area: Start Block :   the start block of the area where no error-bits allowed, 0 based.
No Error Bits Area: Num Blocks :   the number of blocks for this area.
These 2 special features specify an area where no error bits allowed, normally set as 0 and 3. [Default 0 and 3 if listed, otherwise no such area.]

Special Notes

None.

Revision History

V1.0   Jan-26, 2011
            Create this spec.
V1.1   Aug 19th, 2011
            Added the last 3 special features.

Appendix

You can get the file "Description of common NAND special features.pdf" from http://ftp.dataio.com/FCNotes/BBM/