
Qualcomm Multiple Part v2.1 User Manual

General Description and Name

Qualcomm Multiple Part v2.1. This scheme Implements the skip block method for bad block handling but allows the user to create up to 16 partitions in the device. And the data file includes a header which indicates the beginning address of partition table and the beginning address of data.

Relevant User Options

The following special features on the special features tab apply to this scheme. The default values might work in some cases but please make sure to set the right value according to your system.

Please note only the below special feature items are related to this scheme and ignore any others. If any of below items doesn't exist, please check whether the right version has been installed or contact Data I/O for support by submitting Device Support Request through this address:

<http://www.dataio.com/support/dsr.asp>

Bad Block Handling Type = **“Qualcomm Multiple Part v2.1”**

Spare area : Please refer to “Description of common NAND special features.pdf”. *Always set as “Enabled” for this BBM.*[Default ‘Disabled’]

Special Notes

The data file must have a header which includes a partition table.

The spare area is always programmed with the user data in this scheme.

The start block of each partition will be fixed to a particular physical block. If that block is bad, it is acceptable to move to the next good block.

Revision History

V1.0 June 11, 2009
Create this spec.

Appendix

You can get the file “Description of common NAND special features.pdf” from <http://ftp.dataio.com/FCNotes/BBM/>