
RBA BBT BCH16 User Manual

General Description and Name

This scheme has separated all programmable into 3 areas.

- First area “BootRom”: from block 0 – 31. Ignore bad block. But make sure each 8 blocks contain at least one good block.
- Second area: from block 32 – 1001 block. Bad block encounter use RBA instead. (End to front).
- Third area “BB Management info”: From block 1002-1003. Skip use next good block once bad block detected.

All flash has protected by BCH16. Every 1024 Bytes has 28 Bytes of ECC. Please pay attention, data layout has to be converting to address ECC data.

Relevant User Options

The following special features on the special features tab apply to this scheme. The default values might work in some cases but please make sure to set the right value according to your system.

Please note only the below special feature items are related to this scheme and ignore any others. If any of below items doesn't exist, please check whether the right version has been installed or contact Data I/O for support by submitting Device Support Request through this address:

<http://www.dataio.com/support/dsr.asp>

Bad Block Handling Type = “RBA BBT BCH16”

Spare area : “ECC” or “Enable” Please refer to “Description of common NAND special features.pdf”. *Normally set as “ECC” for this BBM.* [Default ‘Disabled’]
If your data file contain context with converted layout and ECC. Please set as “Enable”.
If your data file only contains Main area, expecting to extend layout and calculate ECC.
Please set as “ECC”

Special Notes

Revision History

V1.0 21 Sep. 2018
Create this spec.

Appendix

You can get the file “Description of common NAND special features.pdf” from <http://ftp.dataio.com/FCNotes/BBM/>