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## **Skip DBBT MPF BCH8 iMX6 User Manual**

This is a Multiple Partition format scheme. Any bad blocks within the first two partitions (FCB & DBBT) are ignored (up to “iMX FW Starting Block #”). The rest of partitions use “Skip bad block” method within each partition. Any bad block found within any of the partitions (3..16) will not cause relocation to the starting block of the follow up partitions.

### **Relevant User Options**

The following special features on the special features tab apply to this scheme. The default values might work in some cases but please make sure to set the right value according to your system.

Please note only the below special feature items are related to this scheme and ignore any others. If any of below items doesn't exist, please check whether the right version has been installed or contact Data I/O for support by submitting Device Support Request through this address: <http://www.dataio.com/support/dsr.asp>

**Bad Block Handling Type:** Skip DBBT MPF BCH8 iMX6, Default: None”

**Spare Area:** “Disabled, Default = Disabled”

**PartitionTable File:** C:\PartitionTable.mbn

**Error bits allowed in one page:** How many error bits allowed within one page while verify, this depends on the features of NAND (SLC, MLC ...). [Normally required, default is 0].

**iMX FCB Copies** “4, Default = 4”

The data at page 0 of the binary image starting at offset 0x00 will be duplicated in the blocks 0 thru 3 of the NAND device.

**iMX DBBT Create?** “Enabled”, Default = “Enabled”

When this option is enabled, Discovered Bad Block Tables (DBBT) are added.

**iMX DBBT Copies (if enabled)** “4, Default = 4”

Four copies of DBBT table (including CRC and ECC) will be generated and programmed into the NAND device.

**iMX FW Starting Block #:** “8, Default = 8”

This FW starting block # has to be greater or equal to FCB & DBBT values.

**iMX DBBT last partition included in DBBT:** “4, Default=4”

Default value 4 means DBBT only cover partition2&3(Boot1&Boot2), as partition0&1 not allowed bad block. Partition over or equals 4 is not covered in DBBT.

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## **Image Preparation:**

**FCB Table:** The FCB table (one copy) should be included in the binary image file at address offset of 0x00. **TLWin** will duplicate this FCB table (n) four times into blocks 0 thru 4.

### **Partition Table Format Partition.mbn**

- A binary file of YourFile.MBN with fixed length of 256 bytes.
- Organization: 16 rows x 4 columns. Each table item is 32-bits, little endian byte ordering.
- Each row of the table describes configuration for one partition. Up to 16 partitions can be used.
- Partition configuration:
  - i. **Start Adr:** address of start of partition in flash blocks. The programmer will set the file read pointer and the programmer write pointer to Start Adr. If Start Adr is 0xFFFFFFFF, skip to the next partition.
  - ii. **End Adr:** last valid block in the current partition. The last data block programmed must be equal to or less than **End Adr**, otherwise the programmer will reject the flash device.
  - iii. **Actual Data Length:** number of blocks of data to read from the input file and write to the flash in the current partition.
  - iv. **Note:** For optimal option, the following example should be used and the 3<sup>rd</sup> to the last partitions should be adjusted as needed.

## **Appendix**

You can get the file “Description of common NAND Special Features.pdf” from <http://ftp.dataio.com/FCNotes/BBM/>