
Skip With Block0 BBT User Manual User Manual

General Description and Name

This BBM skip bad block while bad block meet.
Block 0 are contain BBT table, and this will be update during programming.
From block 1 to block4 are reserved block, at least one good block should guarantee, otherwise will fail device.
Whole main are protected by BCH 8 ECC.

Relevant User Options

The following special features on the special features tab apply to this scheme. The default values might work in some cases but please make sure to set the right value according to your system.
Please note only the below special feature items are related to this scheme and ignore any others. If any of below items doesn't exist, please check whether the right version has been installed or contact Data I/O for support by submitting Device Support Request through this address:
<http://www.dataio.com/support/dsr.asp>

Bad Block Handling Type = "Skip With Block0 BBT"

Spare area : Please refer to "Description of common NAND special features.pdf". *Normally set as "Disable" for this BBM (or "enable" depend on data file)*

Special Notes

- a. Current BBM only support 8 bit, 2048+64 page size.
- b. Only 'Spare area' = 'Enable' or 'Disable' are support by this BBM.

Revision History

V1.0 January 8th, 2015
Create this spec.

Appendix

- You can get the file "Description of common NAND special features.pdf" from <http://ftp.dataio.com/FCNotes/BBM/>