

Skip With Main and Spare ECC User Manual

General Description and Name

This scheme Implements the skip block method. Calculate 4 bytes ECC per 512 bytes data, store in the spare area; also calculate ECC of front 60 bytes spare area and store in the last 4 bytes. Calculated or not based on page unit(2K), the blank page (all 0xFF) is not calculated.

Relevant User Options

The following special features on the special features tab apply to this scheme. The default values might work in some cases but please make sure to set the right value according to your system.

Please note only the below special feature items are related to this scheme and ignore any others. If any of below items doesn't exist, please check whether the right version has been installed or contact Data I/O for support by submitting Device Support Request through this address:

<http://www.dataio.com/support/dsr.asp>

Bad Block Handling Type = "Skip With Main and Spare ECC"
Spare area = "Enable" Or "Disable" depend on your PC file:
"Enable" for Data file contains Spare Area data,
"Disable" for Data file does not contain Spare Area.

Required good block area: Start block = "0"

Required good block area: Number of blocks = "0"

These 2 SFs define the area guaranteed to be good,
Please refer to "Description of common NAND
special features.pdf" for more information.

Special Notes

Revision History

V1.0 Date: 2026-07-20

Create this spec.

Appendix

You can get the file "Description of common NAND special features.pdf" from

<http://ftp.dataio.com/FCNotes/BBM/>